DYNAMICAL MODELLING VALIDATION AND CONTROL DEVELOPMENT FOR THE NEW HIGH-DYNAMIC DOUBLE-CRYSTAL MONOCHROMATOR (HD-DCM-Lite) FOR SIRIUS/LNLS

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Sirius Status – Phase 1















Introduction



• HD-DCM-Lite.



Systems Engineering applied in a Monochromator



BRAZILIAN GOVERNMENT

UNITING AND REBUILDING



General System Architecture – Short-Stroke



The general architecture for Short-Stroke is as follow:





System Identification







System Identification – Short-Stroke



System identification





Experimental plant matched the designed model.







Controller Design- Short-Stroke





Frequency domain charts: STABILITY



Bandwidth and margins: ROBUSTNESS

Performance parameter	Target	GAP (reached)	PTC (reached)	RLL (reached)
Bandwidth	200 Hz	226 Hz	287 Hz	173 Hz
Modulus margin	< 10 dB	8.80 dB	8.97 dB	8.78 dB
Phase margin	> 30º	35.96º	31.73º	34.02
Gain margin	> 6 dB	4.53 dB	4.10 dB	5.82 dB

InPosition stability:
• GAP: < 1nm (RMS)
• PTC: < 6nrad (RMS).
• RLL: <7nrad (RMS).





General System Architecture – Rotary Stages

The general architecture for goniometers (rotary stages), by the moment actuation strategy, is as follow : (diagram for a single goniometer)







System Identification and Controller Design – Rotary Stages





Identified plant after exciting a single goniometer:

Then the controller can be designed in the same way of ShS, with a lower bandwidth target (~30 Hz)

Applied blocks







Features in closed-loop - Rotary Stages



The following features were applied for goniometers:





THANK YOU

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