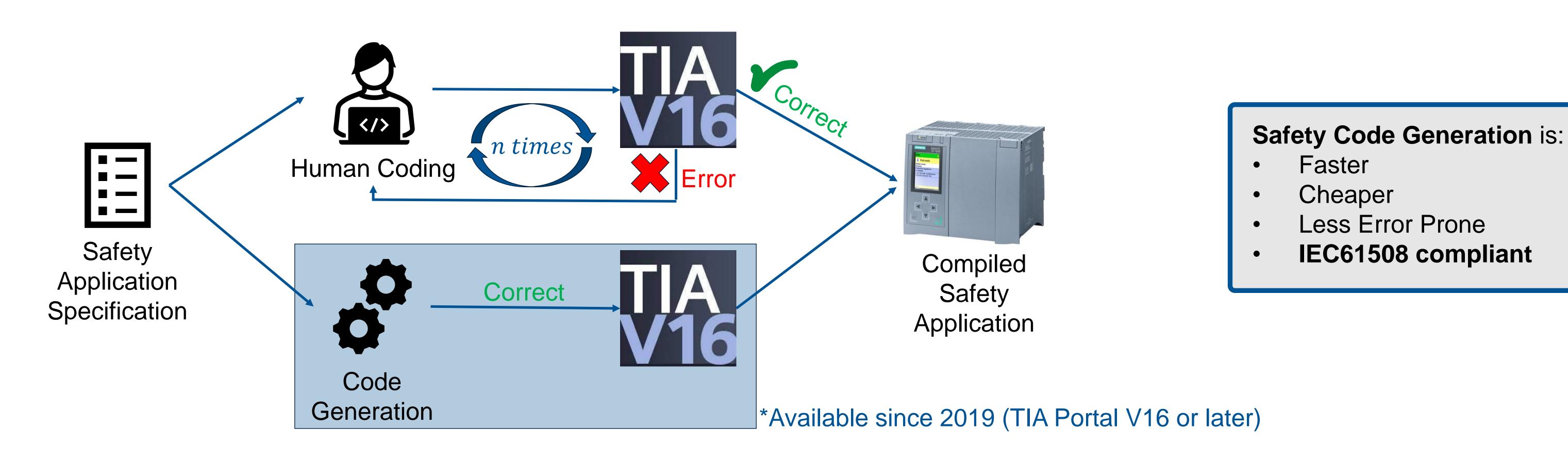


TOWARDS AUTOMATIC GENERATION OF FAIL-SAFE PLC CODE COMPLIANT WITH FUNCTIONAL SAFETY STANDARDS

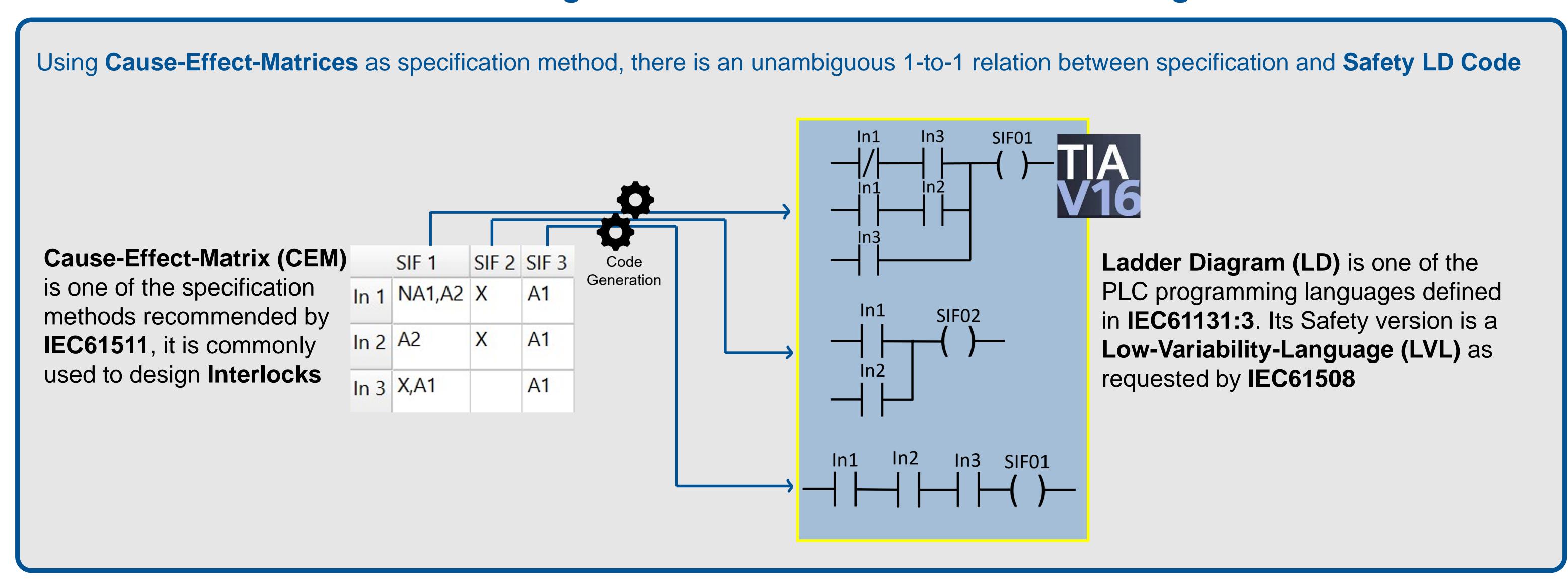
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THPDP059

Context: Safety Application design for PLC



CEM-to-LD: A code generation tool from CEM to Ladder Diagram



Full Workflow

